



High Performance Systems *Details*

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CSC5001 - September 2024





Objectives

- Presents the challenges, constraints and requirements of HPC
- Design an efficient parallel application on an target architecture from its sequential version using the classic tools of parallelism
 - Multi-core, Cluster, heterogeneous CPU+GPU architectures
 - Tools : OpenMP, MPI, CUDA
- Analyze the performance of a parallel application to propose Improvements after having presented the approach and the results

The logo consists of four blue square icons: a stylized globe, a network of nodes, a group of people, and a building.

Teaching team

- **Coordinator**
 - Elisabeth Brunet
- **Teachers**
 - Elisabeth Brunet
 - François Trahay

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+ Lecturers : Qarnot, CEA, DDN



Schedule

- **Courses**
 - Introduction
 - Parallel algorithmic
- **Courses with labs targeting parallel technologies**
 - OpenMP
 - Cache, SIMD
 - MPI
 - CUDA
 - Performance analysis
- **Conferences**
 - Qarnot Computing : Heating a house with computation
 - CEA : HPC at CEA
 - DDN : Data Direct Networks Storage



Project

- **Coming soon !**

**Welcome
and have fun !**